1. **Yoshifumi Nishio :** Oscillator Circuits; Frontiers in Design, Analysis and Applications, Institute of Engineering and Technology (IET), London, Nov. 2016.
2. **Yoko Uwate *and* Yoshifumi Nishio :** Coupled Oscillator Networks with Frustration, --- Oscillator Circuits; Frontiers in Design, Analysis and Applications, Yoshifumi Nishio (Ed.), pp. 163-181 ---, Institute of Engineering and Technology (IET), London, Nov. 2016.
3. **橋爪 正樹, 伊喜利 勇貴, 小西 朝陽, 四柳 浩之, Shyue-Kung Lu :** バウンダリスキャンテスト機構を用いたはんだ接合部の電気検査法とその組込型検査回路, *エレクトロニクス実装学会誌,* **Vol.19,** *No.3,* 161-165, 2016年.
4. **Shyue-Kung Lu, Cheng-Ju Tsai *and* Masaki Hashizume :** Enhanced Built-In Self-Repair Techniques for Improving Fabrication Yield and Reliability of Embedded Memories, *IEEE Transactions on Very Large Scale Integration (VLSI) Systems,* **Vol.24,** *No.8,* 2726-2734, 2016.
5. **(名) Widiant, Masaki Hashizume, Shohei Suenaga, Hiroyuki Yotsuyanagi, Akira Ono, Shyue-Kung Lu *and* Zvi Roth :** A Built-in Test Circuit for Electrical Interconnect Testing of Open Defects in Assembled PCBs, *IEICE Transactions on Information and Systems,* **Vol.E99-D,** *No.11,* 2723-2733, 2016.
6. **Su KuanWu, Leu JenqShiou, Yu MinChieh, Wu YongTing, Lee EauChung *and* Tian Song :** Design and Implementation of Various File Deduplication Schemes on Storage Devices, *Mobile Networks and Applications,* **Vol.22,** *No.1,* 40-50, 2017.
7. **Wen Shi, Tian Song, Takafumi Katayama, Xiantao Jiang *and* Takashi Shimamoto :** Hardware Implementation-Oriented Fast Intra-Coding Based on Downsampling Information for HEVC, *Journal of Real-Time Image Processing,* 1-15, 2017.
8. **Seiya Kita, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization State of Chaotic Circuit Containing Time Delay in One Direction, *Journal of Signal Processing,* **Vol.20,** *No.4,* 125-128, 2016.
9. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Investigation of Optimal Ratio of Males to Females in Firefly Algorithm, *Journal of Signal Processing,* **Vol.20,** *No.4,* 153-156, 2016.
10. **Xiantao Jiang, Tian Song, Wen Shi, Takafumi Katayama, Takashi Shimamoto *and* Lisheng Wang :** Fast Coding Unit Size Decision Based on Probabilistic Graphical Model in High Efficiency Video Coding Inter Prediction, *IEICE Transactions on Information and Systems,* **Vol.E99-D,** *No.11,* 2836-2839, 2016.
11. **Takumi Miyabe, Masaki Hashizume, Hiroyuki Yotsuyanagi, Shyue-Kung Lu *and* Zvi Roth :** A Built-in Electrical Test Circuit for Detecting Open Leads in Assembled PCB Circuits with RC Integrator, *Proceedings of International Conference on Electronics Packaging 2016,* 451-455, Sapporo, Apr. 2016.
12. **Takafumi Katayama, Tian Song, Wen Shi, Takashi Shimamoto *and* Jenq-Shiou Leu :** Reference Frame Selection Algorithm of HEVC Encoder for Low Power Video Device, *Proceedings of 2nd International Conference on Intelligent Green Building and Smart Grid (IGBSG 2016),* 34-39, Praha, Jun. 2016.
13. **Liu Chun-Yao, Chen Yi-Siang, Leu Jenq-Shiou *and* Tian Song :** Energy Efficient Streaming for Smartphones by Video Adaptation and Backlight Control, *The Proceedings of the 2nd International Conference on Intelligent Green Building and Smart Grid (IGBSG),* 124-128, Prague, Czech Republic, Jun. 2016.
14. **Yoshiki Ito, Wen Shi, Tian Song *and* Takashi Shimamoto :** An Adaptive Search Range Selection Algorithm for HEVC, *Proceedings of International Technical Conference on Circuits/Systems, Computers and Communications(ITC-CSCC2016),* 211-214, Okinawa, Jul. 2016.
15. **Ryo Kuroda, Wen Shi, Tian Song *and* Takashi Shimamoto :** Hardware Oriented Early CU Splitting Algorithm by Coding Unit Feature Analysis for HEVC, *Proceedings of International Technical Conference on Circuits/Systems, Computers and Communications(ITC-CSCC2016),* 217-220, Okinawa, Jul. 2016.
16. **Kazuki Kuroda, Takafumi Katayama, Tian Song *and* Takashi Shimamoto :** Adaptive Mode Selection for Low Complexity Enhancement Layer Encoding of SHVC, *Proceedings of International Technical Conference on Circuits/Systems, Computers and Communications(ITC-CSCC2016),* 355-358, Okinawa, Jul. 2016.
17. **Masashi Okamoto, Akihiro Odoriba, Hiroyuki Yotsuyanagi, Masaki Hashizume *and* Shyue-Kung Lu :** A Built-in Test Circuit to Monitor Changing Process of Resistive Open Defects in 3D ICs, *Proc. of International Technical Conference on Circuits/Systems, Computers and Communications 2016,* 295-298, Okinawa, Jul. 2016.
18. **Kouhei Ohtani, Daisuke Suga, Hiroyuki Yotsuyanagi *and* Masaki Hashizume :** A Built-in Test Circuit for Injected Charge Tests of Open Defects in CMOS ICs, *Proc. of International Technical Conference on Circuits/Systems, Computers and Communications 2016,* 291-294, Okinawa, Jul. 2016.
19. **Masaki Hashizume, Yudai Shiraishi, Hiroyuki Yotsuyanagi, Hiroshi Yokiyama, Tetsuo Tada *and* Shyue-Kung Lu :** Electrical Test of Resistive and Capacitive Open Defects at Data Bus in 3D Memory IC, *Proc. of International Design and Concurrent Engineering Conference 2016,* Langkawi, Sep. 2016.
20. **Fara Binti Ali Ashikin, Akihiro Odoriba, Masaki Hashizume, Hiroyuki Yotsuyanagi *and* Shyue-Kung Lu :** Electrical Tests for Capacitive Open Defects in Assembled PCBs, *Proc. of International Design and Concurrent Engineering Conference 2016,* Langkawi, Sep. 2016.
21. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Chaos Propagation in Coupled Chaotic Circuits with Multi-Ring Combination, *Proceedings of IEEE Asia Pacific Conference on Circuits and Systems (APCCAS'16),* 65-68, Jeju, Oct. 2016.
22. **Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization in Complex Networks by Coupled Parametrically Excited Oscillators with Parameter Mismatch, *Proceedings of IEEE Asia Pacific Conference on Circuits and Systems (APCCAS'16),* 69-72, Jeju, Oct. 2016.
23. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Firefly Algorithm Existing Leader Fireflies, *Proceedings of IEEE Asia Pacific Conference on Circuits and Systems (APCCAS'16),* 313-316, Jeju, Oct. 2016.
24. **Seiya Kita, Yoko Uwate *and* Yoshifumi Nishio :** Switching Synchronization States of a Ring of Coupled Chaotic Circuits with One-Direction Delay Effects, *Proceedings of IEEE Asia Pacific Conference on Circuits and Systems (APCCAS'16),* 384-387, Jeju, Oct. 2016.
25. **Hai Minh Tran, Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in Star-Coupled van der Pol Oscillators by Adding Different Frequency Oscillators, *Proceedings of IEEE Asia Pacific Conference on Circuits and Systems (APCCAS'16),* 629-632, Jeju, Oct. 2016.
26. **Masaki Hashizume, Akihiro Odoriba, Hiroyuki Yotsuyanagi *and* Shyue-Kung Lu :** A Built-in Defective Level Monitor of Resistive Open Defects in 3D ICs with Logic Gates, *Proc. of IEEE CPMT Symposium Japan 2016,* 99-102, Kyoto, Nov. 2016.
27. **Kouhei Ohtani, Masaki Hashizume, Daisuke Suga, Hiroyuki Yotsuyanagi *and* Shyue-Kung Lu :** A Power Supply Circuit for Interconnect Tests Based on Injected Charge Volume of 3D IC, *Proc. of IEEE CPMT Symposium Japan 2016,* 139-140, Kyoto, Nov. 2016.
28. **Ali Ashikin Binti Fara, Masaki Hashizume, Yuki Ikiri, Hiroyuki Yotsuyanagi *and* Shyue-Kung Lu :** Testability for Resistive Open Defects by Electrical Interconnect Test of 3D ICs without Boundary Scan Flip Flops, *Proc. of IEEE CPMT Symposium Japan 2016,* 137-138, Kyoto, Nov. 2016.
29. **Shyue-Kung Lu, Shang-Xiu Zhong *and* Masaki Hashizume :** Enhancement of Flash MemoriesAdaptive ECC Techniques for Yield and Reliability, *Proc. of 2016 IEEE 25th Asian Test Symposium,* 287-292, Hiroshima, Nov. 2016.
30. **Takafumi Katayama, Wen Shi, Tian Song *and* Takashi Shimamoto :** Early Depth Determination Algorithm for Enhancement Layer Intra Coding of SHVC, *Proceedings of IEEE International Technical Conference TENCON 2016,* 3083-3086, Singapore, Nov. 2016.
31. **Takumi Kawaguchi, Hiroyuki Yotsuyanagi *and* Masaki Hashizume :** On Control Circuit and Observation Conditions for Testing Multiple TSVs Using Boundary Scan Circuit with Embedded TDC, *Proc. of the 17th IEEE Workshop on RTL and High Level Testing,* 1-3-1-1-3-6, Hiroshima, Nov. 2016.
32. **Fara Ashikin Binti Ali, Yuki Ikiri, Masaki Hashizume, Hiroyuki Yotsuyanagi *and* Shyue-Kung Lu :** Capacitive Open Defect Detection by Electrical Interconnect Test of 3D ICs without Boundary Scan Flip Flops, *Proc. of the 17th IEEE Workshop on RTL and High Level Testing,* 1-2-1-1-2-6, Hiroshima, Nov. 2016.
33. **Chihiro Ikuta, Yoko Uwate *and* Yoshifumi Nishio :** Multilayer Perceptron Including Different Amplitude Random Noise, *Proceedings of International Symposium on Nonlinear Theory and its Applications (NOLTA'16),* 44-47, Yugawara, Nov. 2016.
34. **Ryoji Fukumasa, Masayuki Yamauchi *and* Yoshifumi Nishio :** Investigation of Attracting Force to Synchronization States on Coupled Oscillator System by Using Electric Power, *Proceedings of International Symposium on Nonlinear Theory and its Applications (NOLTA'16),* 385-388, Yugawara, Nov. 2016.
35. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Hybrid Method of Genetic Algorithm and Firefly Algorithm Distinguishing Between Males and Females, *Proceedings of International Symposium on Nonlinear Theory and its Applications (NOLTA'16),* 542-545, Yugawara, Nov. 2016.
36. **Yoko Uwate, Yoshifumi Nishio *and* Ruedi Stoop :** Synchronization in Dynamical Polygonal Oscillatory Networks with Switching Topology, *Proceedings of International Symposium on Nonlinear Theory and its Applications (NOLTA'16),* 578-581, Yugawara, Nov. 2016.
37. **Ryuta Yoshimura, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Denoising Auto Encoder with Logistic Map, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 57-60, Guam, Feb. 2017.
38. **Masaki Takeuchi, Thomas Ott, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** K-Means Algorithm Using Improved Firefly Algorithm, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 225-228, Guam, Feb. 2017.
39. **Masaki Moriyama, Masaki Takeuchi, Yoko Uwate *and* Yoshifumi Nishio :** Analysis of Firefly Algorithm Combined with Chaotic Map, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 229-232, Guam, Feb. 2017.
40. **Takahisa Ando, Yoko Uwate *and* Yoshifumi Nishio :** Cellular Neural Networks with Switching Three Templates for Image Processing, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 257-260, Guam, Feb. 2017.
41. **Kazuki Ueta, Seiya Kita, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in Complex Networks of van der Pol Oscillators, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 285-288, Guam, Feb. 2017.
42. **Daiki Nariai, Hai Minh Tran, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization in Two Rings of Coupled Three van der Pol Oscillators, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 289-292, Guam, Feb. 2017.
43. **Katsuki Nakashima, Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization of Coupled Two Rings of Chaotic Circuits, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 325-328, Guam, Feb. 2017.
44. **Shuhei Hashimoto, Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in Complex Networks of Coupled Chaotic Circuits with Different Degree Distribution, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 333-336, Guam, Feb. 2017.
45. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Investigation of Spreading Chaotic Behavior in Coupled Chaotic Circuit Networks with Various Features, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 337-340, Guam, Feb. 2017.
46. **Kazuki Nagao, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Improvement of Learning Efficiency Using Noise for Back Propagation in Deep Learning, *Proceedings of RISP International Workshop on Nonlinear Circuits, Communications and Signal Processing (NCSP'17),* 397-400, Guam, Feb. 2017.
47. **Zheng-Hong Cai, Hiroyuki Yotsuyanagi *and* Masaki Hashizume :** A Modified PRPG for Test Data Reduction Using BAST Structure, *Proc. RISP International Workshop on Nonlinear Circuit and Signal Processing,* 441-444, Guam, Mar. 2017.
48. **Masaki Hashizume, Hiroyuki Yotsuyanagi, Hiroshi Yokoyama, Tetsuo Tada *and* Shyue-Kung Lu :** Test Input Vectors for Detecting Stuck-at Faults at Address and Data Buses in 3D Stacked Memory ICs, *Proc. of International Forum on Advanced Technologies 2017,* 127-129, Hualien, Taiwan, Mar. 2017.
49. **Michiya Kanda, Masaki Hashizume, Akihiro Odoriba, Yohei Kakee, Hiroyuki Yotsuyanagi *and* Shyue-Kung Lu :** A Built-in Test Circuit Using A Comparator of Offset Cancel Type for Electrical Interconnect Tests of 3D Stacked ICs, *Proc. of International Forum on Advanced Technologies 2017,* 233-235, Hualien, Taiwan, Mar. 2017.
50. **Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization of Coupled Oscillators Containing Ring and Star, *電子情報通信学会 NOLTAソサイエティ大会 講演論文集, No.A-15,* NLS15, Jun. 2016.
51. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Female Percentage in Firefly Algorithm, *電子情報通信学会 NOLTAソサイエティ大会 講演論文集, No.A-17,* NLS17, Jun. 2016.
52. **Hai Minh Tran, Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena of Star-Coupled van der Pol Oscillators with Additional Oscillators, *電子情報通信学会 NOLTAソサイエティ大会 講演論文集, No.A-18,* NLS18, Jun. 2016.
53. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Process of Chaos Propagation in Coupled Chaotic Circuits, *電子情報通信学会 NOLTAソサイエティ大会 講演論文集, No.B-18,* NLS41, Jun. 2016.
54. **川口 晃弘, 島本 隆, 宋 天 :** アンテナ効果を削減する多層配線に関する研究, *電気関係学会四国支部連合大会講演論文集, No.1-29,* 29, 2016年9月.
55. **山野本 武, 島本 隆, 宋 天 :** 積層信号TSV活用のための熱源探索に関する研究, *電気関係学会四国支部連合大会講演論文集, No.1-30,* 30, 2016年9月.
56. **鎌田 祐毅, 島 康平, 島本 隆, 宋 天 :** HEVCにおけるDCT係数の差分符号化による符号化効率向上の性能評価, *電気関係学会四国支部連合大会講演論文集, No.13-22,* 173, 2016年9月.
57. **李 敏, 石 文, 島本 隆, 宋 天 :** HEVCのハードウェア実装におけるTUサイズ選択手法の性能評価, *電気関係学会四国支部連合大会講演論文集, No.13-23,* 174, 2016年9月.
58. **榎木 一史, 片山 貴文, 黒田 一樹, 島本 隆, 宋 天 :** 携帯端末向け参照フレーム削減によるHEVCの低消費電力実現に関する研究, *電気関係学会四国支部連合大会講演論文集, No.13-24,* 175, 2016年9月.
59. **伊喜利 勇貴, 南原 康亮, 四柳 浩之, 橋爪 正樹, Shyue-Kung Lu :** nMOSダイオード組込型検査回路を用いたIC間配線の電気検査用不良判定しきい値の導出, *電気関係学会四国支部連合大会講演論文集,* 83, 2016年9月.
60. **岡本 匡史, 踊場 明宏, Fara Binti Ali Ashikin, 四柳 浩之, 橋爪 正樹, Shyue-Kung Lu :** ダイオードとnMOSからなる電気検査回路による容量断線の実験による検出可能性評価, *電気関係学会四国支部連合大会講演論文集,* 84, 2016年9月.
61. **踊場 明宏, 四柳 浩之, 橋爪 正樹, Shyue-Kung Lu :** 組込み型電気検査回路による3D IC内容量断線の進行過程検出の可能性, *電気関係学会四国支部連合大会講演論文集,* 85, 2016年9月.
62. **田中 聖也, 月本 功, 四柳 浩之, 橋爪 正樹 :** 電流テストによる出力リード浮き検出時の隣接配線からの交流電界印加電圧, *電気関係学会四国支部連合大会講演論文集,* 86, 2016年9月.
63. **大谷 航平, 菅 大介, 四柳 浩之, 橋爪 正樹 :** 電荷注入量による断線不良検出の回路規模に対する影響調査, *電気関係学会四国支部連合大会講演論文集,* 87, 2016年9月.
64. **掛江 庸平, 四柳 浩之, 橋爪 正樹 :** 実験によるIC内組込み型IDDT出現時間検出回路の断線検出可能性調査, *電気関係学会四国支部連合大会講演論文集,* 88, 2016年9月.
65. **河塚 信吾, 四柳 浩之, 橋爪 正樹 :** TDC組込み型スキャンFFの遅延分解能へのばらつきの影響調査, *電気関係学会四国支部連合大会講演論文集,* 92, 2016年9月.
66. **伊勢 幸太郎, 四柳 浩之, 橋爪 正樹, 樋上 喜信, 高橋 寛 :** 隣接線の信号遷移を用いる半断線故障判別法の断線位置に対する有効性調査, *電気関係学会四国支部連合大会講演論文集,* 93, 2016年9月.
67. **藤谷 和依, 四柳 浩之, 橋爪 正樹 :** 論理値割当隣接線選択と故障顕在化しきい値の断線故障用テスト生成時間への影響, *電気関係学会四国支部連合大会講演論文集,* 94, 2016年9月.
68. **三好 大地, 四柳 浩之, 橋爪 正樹 :** IDDT出現時間を用いる断線故障検査法の伝搬不能故障検出に対する有効性調査, *電気関係学会四国支部連合大会講演論文集,* 95, 2016年9月.
69. **森 亮介, 四柳 浩之, 橋爪 正樹 :** TDC 組込み型バウンダリスキャンを用いる遅延故障検査でのチップ間ばらつき補正, *電気関係学会四国支部連合大会講演論文集,* 96, 2016年9月.
70. **Katsuki Nakashima, Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Dynamic Behavior of Coupled Two Rings of Chaotic Circuits, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-6,* 6, Sep. 2016.
71. **Seiya Kita, Yoko Uwate *and* Yoshifumi Nishio :** Switching Synchronization State of System Including Time Delay in One Direction, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-7,* 7, Sep. 2016.
72. **小林 加奈, 細川 康輝, 上手 洋子, 西尾 芳文 :** メモリスタで結合したカオス回路における同期現象と誤差の関係, *電気関係学会四国支部連合大会講演論文集, No.1-10,* 10, 2016年9月.
73. **Daiki Nariai, Hai Minh Tran, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in Two Rings of van der Pol Oscillators, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-11,* 11, Sep. 2016.
74. **Hai Minh Tran, Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena and Oscillation Death of Star-Coupled van der Pol Oscillators with Additional Oscillators, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-12,* 12, Sep. 2016.
75. **Shuhei Hashimoto, Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Penomena of Coupled Chaotic Circuits with Different Degree Distribution, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-13,* 13, Sep. 2016.
76. **Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization in Complex Networks of Parametrically Excited Oscillators with Parameter Mismatch, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-14,* 14, Sep. 2016.
77. **Ryota Oshima, Yoko Uwate *and* Yoshifumi Nishio :** Neural Firing of Coupled Izhikevich Neurons with Bug Neuron, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-19,* 19, Sep. 2016.
78. **Yuki Fujisawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in a Ring of Cubic Maps Containing Delay Coupling, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-20,* 20, Sep. 2016.
79. **佐藤 柊, 生田 智敬, 中村 雄一, 上手 洋子, 西尾 芳文 :** カオスを用いた選択的学習を行うニューラルネットワーク, *電気関係学会四国支部連合大会講演論文集, No.1-22,* 22, 2016年9月.
80. **Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Feed-Forward Neural Networks with Temporal Change in Gradient of Sigmoid Functions, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-23,* 23, Sep. 2016.
81. **Ryuta Yoshimura, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Deep Learning Using Dropout by Intermittency Chaos, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-24,* 24, Sep. 2016.
82. **Kazuki Nagao, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Deep Learning Using Noise for Back Propagation, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-25,* 25, Sep. 2016.
83. **Takahisa Ando, Yoko Uwate *and* Yoshifumi Nishio :** Cellular Neural Networks with Switching Two-Type Templates for Edge Detection, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-34,* 34, Sep. 2016.
84. **Masaki Moriyama, Masaki Takeuchi, Yoko Uwate *and* Yoshifumi Nishio :** Firefly Algorithm with Bernoulli Shift Map, *Journal of Shikoku-Section Joint Convention of the Institutes of Electrical and Related Engineers, No.1-36,* 36, Sep. 2016.
85. **河塚 信吾, 四柳 浩之, 橋爪 正樹 :** 微小遅延故障テストのためのTDC組込み型スキャンFFの設計について, *電子情報通信学会技術研究報告,* **Vol.116,** *No.331,* 105-110, 2016年11月.
86. **二関 森人, 細川 利典, 吉村 正義, 新井 雅之, 四柳 浩之, 橋爪 正樹 :** 到達不能状態を用いたSATベース順序回路のテスト不能故障判定法, *電子情報通信学会技術研究報告,* **Vol.116,** *No.466,* 29-34, 2017年2月.
87. **薮井 大輔, 四柳 浩之, 橋爪 正樹 :** バウンダリスキャンテスト回路を用いた実装基板のオンライン配線試験法, *第31回エレクトロニクス実装学会春季講演大会,* 58-61, 2017年3月.
88. **大谷 航平, 菅 大介, 四柳 浩之, 橋爪 正樹 :** 電荷注入回数によるIC間配線の試験回路, *第31回エレクトロニクス実装学会春季講演大会,* 62-65, 2017年3月.
89. **矢野 雄基, 藤木 良太, 宋 天, 島本 隆, 中村 暢達 :** 深層学習を用いた海鼠認識に関する研究, *電子情報通信学会総合大会講演論文集, No.D-8-9,* 2017年3月.
90. **神原 東風, 四柳 浩之, 橋爪 正樹 :** IDDT出現時間差を用いる検査法のための低遷移パターンの故障検出率調査, *電子情報通信学会総合大会講演論文集,* 139, 2017年3月.
91. **島 康平, 石 文, 宋 天, 島本 隆 :** H.265/HEVCにおけるウォーターマーキング技術に関する研究, *電子情報通信学会総合大会講演論文集, No.D-11-55,* 2017年3月.
92. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Chaos Propagation and Synchronization in Double Ring and Single Ring Model, *International Workshop on Computer Vision and Signal Processing (CVSP'16),* Apr. 2016.
93. **Naoto Kanazawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization of Hindmarsh Rose Neurons by Temporal Evolution of Coupling Strengths, *International Workshop on Computer Vision and Signal Processing (CVSP'16),* Apr. 2016.
94. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Firefly Algorithm Changing Female Moving Distance, *International Workshop on Computer Vision and Signal Processing (CVSP'16),* Apr. 2016.
95. **Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization of Coupled Oscillators Containing Ring and Star Structures, *International Workshop on Computer Vision and Signal Processing (CVSP'16),* Apr. 2016.
96. **上手 洋子, 西尾 芳文 :** 確率的に結合が切り替わる多角形発振器ネットワークの同期現象, *第29回 回路とシステムワークショップ 論文集,* 261-266, 2016年5月.
97. **金澤 尚人, 上手 洋子, 西尾 芳文 :** ニューロン間の結合強度を時間的に変化させたときの同期現象, *電子情報通信学会 非線形問題研究会 技術報告,* **Vol.NLP2016,** *No.4,* 19-23, 2016年5月.
98. **橘高 新三郎, 上手 洋子, 西尾 芳文 :** シグモイド関数の傾きを減少させたフィードフォワードニューラルネットワークにおけるバックプロパゲーション, *電子情報通信学会 非線形問題研究会 技術報告,* **Vol.NLP2016,** *No.5,* 25-28, 2016年5月.
99. **武内 将希, 松下 春奈, 上手 洋子, 西尾 芳文 :** 雌のパラメータを変化させたときのホタルアルゴリズム, *電子情報通信学会 非線形問題研究会 技術報告,* **Vol.NLP2016,** *No.6,* 29-32, 2016年5月.
100. **上田 和輝, 上手 洋子, 西尾 芳文 :** リング・スター構造を含んだ結合発振器の同期現象, *電子情報通信学会 非線形問題研究会 技術報告,* **Vol.NLP2016,** *No.7,* 33-36, 2016年5月.
101. **チャン ミン ハイ, 上手 洋子, 西尾 芳文 :** 異なる周波数の発振器を付加した結合 van der Pol 発振器系における同期現象, *電子情報通信学会 非線形問題研究会 技術報告,* **Vol.NLP2016,** *No.8,* 37-40, 2016年5月.
102. **近澤 隆啓, 上手 洋子, 西尾 芳文 :** シングルリングとダブルリングモデルにおけるカオス伝搬と同期現象, *電子情報通信学会 非線形問題研究会 技術報告,* **Vol.NLP2016,** *No.9,* 41-44, 2016年5月.
103. **Yoko Uwate, Yuichi Tanji *and* Yoshifumi Nishio :** Torus Hub in Two Parameters Space of Coupled Oscillators with Transmission Line Model, *International Workshop on Nonlinear Dynamics of Electronic Systems (NDES'16),* Jun. 2016.
104. **Yoshifumi Nishio :** Chaos and its Applications, *IEEE CASS Guangzhou and Shikoku Chapters Joint Workshop on Circuits and Systems (GSCAS'16),* Jul. 2016.
105. **Yoko Uwate, Yoshifumi Nishio *and* Ruedi Stoop :** Synchronization in Frustrated Coupled Oscillatory Systems, *IEEE CASS Guangzhou and Shikoku Chapters Joint Workshop on Circuits and Systems (GSCAS'16),* Jul. 2016.
106. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Process of Chaos Propagation as Multi-Ring with Hub Network, *IEEE CASS Guangzhou and Shikoku Chapters Joint Workshop on Circuits and Systems (GSCAS'16),* Jul. 2016.
107. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Firefly Algorithm Focused on Female Moving Distance, *IEEE CASS Guangzhou and Shikoku Chapters Joint Workshop on Circuits and Systems (GSCAS'16),* Jul. 2016.
108. **小濱 佑哉, 四柳 浩之, 橋爪 正樹, 樋上 喜信, 高橋 寛 :** 半断線故障検査時の遷移タイミングを考慮する信号割当隣接線候補の削減について, *第75回FTC研究会資料,* 2016年7月.
109. **河口 巧, 四柳 浩之, 橋爪 正樹 :** TDC 組込み型バウンダリスキャンを用いた複数 TSV の検査用信号の印加と観測について, *第75回FTC研究会資料,* 2016年7月.
110. **Yoshifumi Nishio :** Complex Phenomena in Coupled Chaotic Circuits, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* Jul. 2016.
111. **Takahisa Ando, Yoko Uwate *and* Yoshifumi Nishio :** Cellular Neural Networks with Switching Two-Type Templates for Image Processing, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S1B.5, Jul. 2016.
112. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Features of Firefly Algorithm Inceasing Females, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S1B.7, Jul. 2016.
113. **Norio Inukai, Yoko Uwate *and* Yoshifumi Nishio :** Strategic Particle Swarm Optimization with Predators, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S1B.8, Jul. 2016.
114. **Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Investigation of Synchronization in Complex Networks by Coupled Parametrically Excited Oscillators with Mismatch, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S2A.4, Jul. 2016.
115. **Seiya Kita, Yoko Uwate *and* Yoshifumi Nishio :** Switching Synchronization Phenomena of a Ring of Coupled Chaotic Circuits with One-Direction Time-Delayed Effects, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S2A.6, Jul. 2016.
116. **Daiki Nariai, Hai Minh Tran, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in a Ring of Coupled Three van der Pol Oscillators, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S2B.7, Jul. 2016.
117. **Katsuki Nakashima, Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Dynamics of Coupled Two Rings of Chaotic Circuits, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S3A.3, Jul. 2016.
118. **Yuki Fujisawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization States in Coupled Cubic Maps with Delay Coupling, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S3A.5, Jul. 2016.
119. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Chaos Propagation in Coupled Chaotic Circuits as Multi-Ring with Hub Network, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S3B.1, Jul. 2016.
120. **Shuhei Hashimoto, Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena of Coupled Chaotic Circuits with Degree Distribution Network Topology, *Taiwan and Japan Conference on Circuits and Systems (TJCAS'16),* S3B.7, Jul. 2016.
121. **Zheng-Hong Cai, Hiroyuki Yotsuyanagi *and* Masaki Hashizume :** A Modified PRPG for Test Pattern Generation using BAST structure, *2016 Taiwan and Japan Conference on Circuits and Systems,* Aug. 2016.
122. **Yoshifumi Nishio :** CASS Regional and Local Activities, *2016 CAS Workshop in China (Chengdu),* Sep. 2016.
123. **Yoshifumi Nishio :** CASS Regional and Local Activities, *2016 CAS Workshop in China (Xi'an),* Sep. 2016.
124. **Yoshifumi Nishio :** CASS Regional and Local Activities, *2016 CAS Workshop in China (Beijing),* Sep. 2016.
125. **Yoshifumi Nishio :** CASS Regional and Local Activities, *2016 CAS Workshop in China (Guangzhou),* Sep. 2016.
126. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Investigation of Chaos Propagation in Complex Networks, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
127. **Yuki Fujisawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in a Ring of Cubic Maps with Intermittency Chaos, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
128. **Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Influence of Regional Change in Synchronization of Complex Networks in Coupled Parametrically Excited Oscillators with Parameter Mismatch, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
129. **Misa Sekimoto, Yoko Uwate *and* Yoshifumi Nishio :** Automatic Synchronization Algorithm of Coupled Chaotic Circuits, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
130. **Ryota Oshima, Yoko Uwate *and* Yoshifumi Nishio :** Investigation of the Behavior of Neurons Coupled Izhikevich Neurons with Abnormality, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
131. **Seiya Kita, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization State of a Ring of Coupled One-Direction Time-Delayed Chaotic Circuits, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
132. **Shogo Tamada, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization and Chaos Propagation between Three-periodic and Chaotic Attractor in Network, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
133. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Investigation of Firefly Algorithm Existing Fireflies Flying Slow, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
134. **Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena of Complex Networks Coupled by van der Pol Oscillators, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
135. **Ryuta Yoshimura, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Dropout with Intermittency Chaos, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
136. **Kazuki Nagao, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Deep Learning for Noisy Back Propagation, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
137. **Kana Kobayashi, Yasuteru Hosokawa, Yoko Uwate *and* Yoshifumi Nishio :** Differences as a Coupling Element in a Chaotic System between a Resistor and a Memristor, *IEEE CASS Shanghai and Shikoku Chapters Joint Workshop on Circuits and Systems (SSJW'16),* Nov. 2016.
138. **Kazuki Nagao, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Deep Learning Improving Learning Efficiency by Using Noise for Back Propagation, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 1-4, Dec. 2016.
139. **Takahisa Ando, Yoko Uwate *and* Yoshifumi Nishio :** Cellular Neural Networks with Switching Three-Type Templates for Edge Detection, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 5-8, Dec. 2016.
140. **Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in Frustration Networks of van der Pol Oscillators, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 11-14, Dec. 2016.
141. **Daiki Nariai, Hai Minh Tran, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in Two Rings of Coupled Three van der Pol Oscillators, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 15-17, Dec. 2016.
142. **Yuki Fujisawa, Yoko Uwate *and* Yoshifumi Nishio :** Research of Two Coupled Maps with Intermittency Chaos, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 44-47, Dec. 2016.
143. **Masaki Moriyama, Masaki Takeuchi, Yoko Uwate *and* Yoshifumi Nishio :** Firefly Algorithm Combined with Chaotic Map, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 50-52, Dec. 2016.
144. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Improved Firefly Algorithms Including Slow Fireflies, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 53-56, Dec. 2016.
145. **Chihiro Ikuta, Yoko Uwate *and* Yoshifumi Nishio :** Artificial Neuron-Glia Network Based on Multilayer Perceptron, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 66, Dec. 2016.
146. **Seiya Kita, Yoko Uwate *and* Yoshifumi Nishio :** Switching Synchronization States of Ring System of Chaotic Circuit Including Time Delay in One-Direction, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 67-70, Dec. 2016.
147. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Chaos Propagation of Scale-Free Coupled Chaotic Circuits with Different Topology, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 73-74, Dec. 2016.
148. **Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Influence of Regional Change in Synchronization of Complex Networks in Coupled Parametrically Excited Oscillators, *Proceedings of IEEE Workshop on Nonlinear Circuit Networks (NCN'16),* 75-78, Dec. 2016.
149. **Yoshifumi Nishio :** Complex Phenomena in Coupled Chaotic Circuits, *Invited Lecture at Universiti Putra Malaysia,* Dec. 2016.
150. **Yoshifumi Nishio :** Regional and Local Activities in CASS, *2016 CAS Workshop in Malaysia,* Dec. 2016.
151. **Yoshifumi Nishio :** Regional Activities and Membership Development in IEEE Circuits and Systems Society, *2016 CAS Workshop in Vietnam,* Dec. 2016.
152. **Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in a Network with Ring and Star Structures of van der Pol Oscillators, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'16),* 1, Dec. 2016.
153. **Hai Minh Tran, Kosuke Oi, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena and Oscillation Death in Star-Coupled van der Pol Oscillators by Adding Different Frequency Oscillators, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'16),* 2, Dec. 2016.
154. **Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Making Music Score with Deep Autoencoder, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'16),* 3, Dec. 2016.
155. **Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Spread of Chaotic Behavior in Coupled Chaotic Circuits with Hub Network, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'16),* 4, Dec. 2016.
156. **Masaki Takeuchi, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** Investigation of Firefly Algorithm Including Slow Fireflies, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'16),* 5, Dec. 2016.
157. **錦織 誠, 山崎 紘史, 細川 利典, 吉村 正義, 新井 雅之, 四柳 浩之, 橋爪 正樹 :** 抵抗性オープン故障のテスト生成法の評価, *第76回FTC研究会資料,* 2017年1月.
158. **Takahisa Ando, Yoko Uwate *and* Yoshifumi Nishio :** Cellular Neural Networks with Switching Two Templates for Image Processing, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 1, Feb. 2017.
159. **Masaki Moriyama, Masaki Takeuchi, Yoko Uwate *and* Yoshifumi Nishio :** Research of Firefly Algorithm Combined with Chaotic Map, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 3, Feb. 2017.
160. **Kazuki Nagao, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Improvement of Learning Accuracy and Reduction of Learning Loops by Using Noise in Deep Learning, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 4, Feb. 2017.
161. **Masaki Takeuchi, Thomas Ott, Haruna Matsushita, Yoko Uwate *and* Yoshifumi Nishio :** K-Means Algorithm Using an Improved Firefly Algorithm Applied Simple Toy Model, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 5, Feb. 2017.
162. **Ryuta Yoshimura, Shinsaburo Kittaka, Yoko Uwate *and* Yoshifumi Nishio :** Denoising Auto Encoder with Intermittency Chaos, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 6, Feb. 2017.
163. **Daiki Nariai, Hai Minh Tran, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization in Two Rings of Coupled van der Pol Oscillators, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 7, Feb. 2017.
164. **Katsuki Nakashima, Kazuki Ueta, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena in Coupled Two Rings of Chaotic Circuits, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 8, Feb. 2017.
165. **Shuhei Hashimoto, Takahiro Chikazawa, Yoko Uwate *and* Yoshifumi Nishio :** Synchronization Phenomena by Coupled Chaotic Circuits in Three Types of Network, *Proceedings of IEEE Workshop on Nonlinear Signal Processing (NSP'17),* 9, Feb. 2017.